



## Cadence Design Systems Applications Available on Solaris™ 10 OS

Product Name	Solaris 10 /SPARC	Solaris 10/ x64
10-pack FPGA Compiler for Xtreme III series 7.1	X	X
5-pack FPGA Compiler for Xtreme III series 7.1	X	X
64bit Verifault(R)-XL simulator	X	X
64bit Verifault-XL(R) Slave Node License	X	X
8 FPGA Compiler 7.1	X	X
Additional 10-pack FPGA Compiler for Xtreme III series 7.1	X	X
All Cadence Kits 4.31	X	X
AMBA AHB Assertion Based (Verilog Module) (VIP) 2.2	X	X
AMS Designer with Flexible Analog Simulation 6.2	X	X
AMS Designer with Flexible Analog Simulation 6.1	X	X
AMS Option to Incisive 6.1	X	X
AMS Option to Incisive 6.2	X	X
Assura Design Rule Checker 2.0	X	X
Assura Graphical User Interface Option 2.0	X	X
Assura Layout Vs. Schematic Verifier 2.0	X	X
Assura Multiprocessor Option 2.0	X	X
Assura Parasitic Extractor 2.0	X	X
Assura RCX Field Solver Option 3.0	X	X
Assura RCX High Frequency Option 3.1	X	X
Assura RCX Multiprocessor Option 3.0	X	X
Assura RCX Parasitic Inductance Option 3.0	X	X
Assura RCX Parasitic Inductance: Cell Level Option 3.1	X	X

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Product Name	Solaris10 /SPARC	Solaris / x64
Cadence Constraint Integration Toolbox 6.12	X	X
Cadence Design Framework II 6.1	X	X
Cadence Design Framework Integrator's Toolkit 6.1	X	X
Cadence Export Model Packager 6.1	X	X
Cadence Framework Integration Runtime Option 6.12	X	X
Cadence OASIS for RFDE 6.1	X	X
Cadence RC Network Reducer Option 3.0	X	X
Cadence Simulation Analysis Environment (SimVision) 6.1	X	X
Cadence SKILL Development Environment 6.1	X	X
Cadence Virtuoso Passive Component Designer 6.2	X	X
CDLIN 6.1	X	X
CDSDOC dep~11.24.106~sun4v	X	X
CeltIC NDC Crosstalk Analyzer with Delay Calculator 6.1	X	X
Chameleon Basic 4.3	X	X
Chameleon Basic 4.31	X	X
Chameleon DRC Option 4.3	X	X
Chameleon DRC Option 4.31	X	X
Chameleon IBM Option 4.31	X	X
Chameleon IBM Option 4.3	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Chameleon LV Slave 4.3	X	X
Chameleon LV Slave 4.31	X	X
Chameleon LVS Option 4.3	X	X
Chameleon LVS Option 4.31	X	X
Chameleon PG Option 4.3	X	X
Chameleon PG Option 4.31	X	X
Chameleon PG Slave 4.3	X	X
Chameleon PG Slave 4.31	X	X
Chameleon TI Option 4.3	X	X
Chameleon TI Option 4.31	X	X
Clock Tree Synthesis Option to First Encounter Classic or First Encounter 7.1	X	X
CVD 1.2	X	X
Desktop Option for EMG100 (IEM) and DMG50 (IDTM) 2.0	X	X
DFM Extraction Fire & Ice EXT 6.2	X	X
Diva Design Rule Checker 4.4.5	X	X
Diva Layout Vs. Schematic Verifier 4.4.5	X	X
Diva Parasitic Extractor 4.4.5	X	X
Diva Physical Extraction and Extraction Suite 4.4.5	X	X
Diva Physical Verification Suite 4.4.5	X	X
Dracula Design Rule Checker 4.8	X	X
Dracula Graphical User Interface 6.1	X	X
Dracula Layout Vs. Schematic Verifier 4.8	X	X
Dracula Parasitic Extractor 4.8	X	X
Dracula Physical Verification and Extraction Suite 4.8	X	X
Dracula Physical Verification Suite 4.8	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Dynamic Gate Option to Voltage Storm PE 06.20.003	X	X
eAnalyzer EANL 5.2	X	X
ElectronStorm 06.20.003	X	X
Encounter (TM)Conformal Constraint Designer - L 7.1	X	X
Encounter Conformal (Confrml) 6.2	X	X
Encounter Conformal (Confrml) 7.1	X	X
Encounter Conformal Constraint Designer - XL 7.1	X	X
Encounter Conformal Constraint Designer (CCD) 6.2	X	X
Encounter Conformal Constraint Designer (CCD) 7.1	X	X
Encounter Diagnostics - GXL 6.2	X	X
Encounter Diagnostics - L 6.2	X	X
Encounter Diagnostics - Physical Precision Option 6.2	X	X
Encounter Diagnostics - XL 6.2	X	X
Encounter Diagnostics Engine - XL 6.2	X	X
Encounter Diagnostics Environment - XL 6.2	X	X
Encounter Library Characterizer - GXL 7.1	X	X
Encounter Library Characterizer -XL 7.1	X	X
Encounter RTL Compiler (RC) 5.2	X	X
Encounter RTL Compiler (RC) 7.1	X	X
Encounter RTL Compiler Verification (RCV) 6.1	X	X
Encounter Test (ET) 3.1.7	X	X
Encounter Test Analysis 6.2	X	X
Encounter Test Architect - GXL 6.2	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Encounter Test Architect - L 6.2	X	X
Encounter Test Architect - XL 6.2	X	X
Encounter True-time Test - XL 6.2	X	X
Encounter Test ATPG Plus 6.2	X	X
Encounter Test Compression 6.2	X	X
Encounter Timing System-GXL 7.1	X	X
Encounter Timing System-L 7.1	X	X
Encounter Timing System-XL 7.1	X	X
Encounter True-time Test - GXL 6.2	X	X
Encounter True-time Test - L 6.2	X	X
Encounter True-time Test - XL 6.2	X	X
Encounter X 7.1	X	X
Encounter Xcite/Xtreme rtl verification RCV 6.1	X	X
ESL Option for Incisive Enterprise Simulator 6.1	X	X
FINALE 6.3.0	X	X
FINALE - Chip Optimizer (was Catena) 6.2	X	X
FINALE - Space-based Router (was Catena) 6.2	X	X
FINALE 7.1	X	X
First Encounter - GXL 7.1	X	X
First Encounter - L 7.1	X	X
First Encounter - XL 7.1	X	X
First Encounter Ultra 7.1	X	X
Formal Analysis Option to Incisive 6.11.003	X	X
Formal Analysis Option to Incisive 6.1	X	X
FPGA Compiler Option 7.1	X	X
ICC_dep_other~11.24.106~sun4v	X	X
ICC_Release_Info~11.24.106~sun4v	X	X
Incisive Design Team Manager 2.0	X	X
Incisive Design Team Simulator 6.1	X	X
Incisive Design Team Simulator 6.2	X	X
Incisive Enterprise Manager 2.0	X	X
Incisive Enterprise Simulator (without Methodology) 6.1	X	X
Incisive Enterprise Simulator 6.1	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Incisive Enterprise Specman Elite Testbench 6.1	X	X
Incisive Enterprise Specman ESL Testbench 6.1	X	X
Incisive Enterprise Specman Simulator 6.1	X	X
Incisive Enterprise Specman Simulator Interface for MTI 6.1	X	X
Incisive Enterprise Specman Simulator Interface for VCS 6.1	X	X
Incisive Formal Verifier 6.11.003	X	X
Incisive Formal Verifier (IFV) 6.1	X	X
Incisive HDL Simulator 6.1	X	X
Incisive HDL Simulator 6.2	X	X
Incisive Plan-to-Closure Methodology Option 6.1	X	X
Incisive Plan-to-Closure Methodology option to Specman, Specman ESL & IES 6.1	X	X
Incisive Plan-to-Closure Ready option to DTS and IUS 6.1	X	X
Incisive Regression Package 6.1	X	X
Incisive Specman Simulator SPECSIM 1.3	X	X
Incisive Unified Simulator (NC-* SystemC) 5.82	X	X
Incisive Unified Simulator 6.1	X	X
Incisive Unified Simulator 6.2	X	X
Incisive XE, Palladium (IXE) 4.0	X	X
Incisive XLD 6.1	X	X
Incisive XLD 6.2	X	X
MaskCompose (KMC) 3.8	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Nano Encounter 7.1	X	X
NanoRoute Ultra SoC Routing Solution 7.1	X	X
NC-SC Simulator 6.2	X	X
NC-SC Simulator 6.1	X	X
NC-Sim Mixed-Language Simulator 6.1	X	X
NC-Sim Mixed-Language Simulator 6.2	X	X
NC-Verilog Simulator 6.1	X	X
NC-Verilog Simulator 6.2	X	X
NC-VHDL Simulator 6.1	X	X
NC-VHDL Simulator 6.2	X	X
Open Access Install	X	X
Open Access install 22.04.006	X	X
Open Simulation System 6.1	X	X
OpenAccess 02.02.071	X	X
OpenAccess 2.2.5	X	X
OpenAccess 22.04.006 Inx86 22.04.006	X	X
OpenAccess Installation Doc 02.02.048	X	X
Palladium Graphical Debug User Interface 5.1	X	X
Palladium II Always-on domain license 5.1	X	X
Palladium III System Software 5.1	X	X
Palladium State Machine Viewer 5.1	X	X
Palladium System Software 5.1	X	X
PCI Express Gen 1 End Point UVC latest	X	X
PVS (Physical Verification System) 6.1	X	X
QuickView (KQV) 4.3.1	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Quickview 4.3.7P1	X	X
QuickView Full	X	X
QuickView GDSII	X	X
QuickView MEBES	X	X
QuickView OASIS	X	X
QuickView SemiP10	X	X
QuickView-L	X	X
RCC Compiler for Xtreme/Xcite Xilinx based 7.1	X	X
RCX Advanced process features 3.1	X	X
RelXpert Sony NBTI Model 6.1	X	X
RelXpert Sony NBTI Model 6.2	X	X
Route Accelerator Multi-Treaded Route Op- tion 7.1	X	X
SignalStorm Library Characterizer 6.1	X	X
SignalStorm Library Characterizer Expansion Pack 6.1	X	X
SignalStorm NDCnometer Delay Calculator 6.1	X	X
Simulation Analysis Environment (SimVision) 6.2	X	X
SoC Encounter - GXL 7.1	X	X
SoC Encounter - L 7.1	X	X
SOC Encounter - XL 7.1	X	X
SoC Encounter (SOC) 7.1	X	X
Spectre - Basic Advanced Circuit Simulator 6.1	X	X
Spectre BTA HVMOS Model 6.1	X	X
Spectre BTA HVMOS Model 6.2	X	X
Spectre BTA SOI Model 6.1	X	X
Spectre BTA SOI Model 6.2	X	X
Spectre Dallas Semiconductor Model 6.1	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Spectre Dallas Semiconductor Model 6.2	X	X
Spectre Nortel Models 6.1	X	X
Spectre Nortel Models 6.2	X	X
Spectre Siemens Models 6.1	X	X
Spectre Siemens Models 6.2	X	X
Spectre ST Models 6.1	X	X
Spectre ST Models 6.2	X	X
Spectre-Basic Advanced Circuit Simulator 6.2	X	X
Spectre-RF Cadence SPW Model Link Option 6.1	X	X
Spectre-RF-Cadence SPW Model Link Option to Spectre RF+G199 6.2	X	X
SpeXtreme (SPXT) 1.0	X	X
UVC102: PCI Express Gen 1 End Point UVC	X	X
UVC103: PCI Express Gen 1 Root Complex UVC	X	X
UVCPCI 2.2	X	X
Verifault - XL simulator 3.6	X	X
Verifault-XL simulator 6.2	X	X
Verifault-XL Slave Node License 3.6	X	X
Verifault-XL Slave Node License 6.2	X	X
Verilog - XL Simulator 6.1	X	X
Verilog-XL Simulator 6.2	X	X
Virtuoso Advanced Device Modeling (for HSPICE) 6.2	X	X
Virtuoso Advanced Device Modeling HVMOS (for Eldo) 6.2	X	X
Virtuoso Advanced Simulation Interface Option to Virtuoso Spectre Simulator - L 6.2	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Virtuoso AMS Designer Environment 6.1	X	X
Virtuoso AMS Designer Simulator 6.1	X	X
Virtuoso AMS Designer Simulator 6.2	X	X
Virtuoso Analog Design Environment GXL 6.1	X	X
Virtuoso Analog Design Environment L 6.1	X	X
Virtuoso Analog Design Environment XL 6.1	X	X
Virtuoso Analog ElectronStorm Option 4.1	X	X
Virtuoso Analog HSPICE Interface Option 6.1	X	X
Virtuoso Analog Oasis Run-Time Option 6.1	X	X
Virtuoso Analog VoltageStorm Option 4.1	X	X
Virtuoso Constraint API Run-Time Option 6.1	X	X
Virtuoso Digital Implementation Option 7.1	X	X
Virtuoso EDIF 200 Reader 6.1	X	X
Virtuoso EDIF 200 Writer 6.1	X	X
Virtuoso Layout Migrate 6.1	X	X
Virtuoso Layout Suite - GXL 6.1.2	X	X
Virtuoso Layout Suite GXL 6.1	X	X
Virtuoso Layout Suite L 6.1	X	X
Virtuoso Layout Suite XL 6.1	X	X
Virtuoso Multi-mode Simulation 6.1	X	X
Virtuoso Multi-mode Simulation 6.2	X	X
Virtuoso OASIS Integration Toolbox 6.12	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Virtuoso Phase Designer 23.1	X	X
Virtuoso Phase Designer 3.1	X	X
Virtuoso RelXpert 6.1	X	X
Virtuoso RelXpert 6.2	X	X
Virtuoso Schematic Editor HSPIICE Interface 6.1	X	X
Virtuoso Schematic Editor L 6.1	X	X
Virtuoso Schematic Editor Verilog Interface 6.1	X	X
Virtuoso Schematic Editor XL 6.1	X	X
Virtuoso Schematic VHDL Interface 6.1	X	X
Virtuoso Simulation Environment 6.1	X	X
Virtuoso Spctre Circuit Simulator 6.1	X	X
Virtuoso Spectre Circuit Simulator 6.2	X	X
Virtuoso Spectre Model Interface Option 6.1	X	X
Virtuoso Spectre Model Interface Option 6.2	X	X
Virtuoso Spectre Simulator - XL 6.2	X	X
Virtuoso Spectre Simulator 6.1	X	X
Virtuoso Spectre Simulator 6.2	X	X
Virtuoso Spectre-RF Option for 38500 6.1	X	X
Virtuoso Spectre-RF Option for 38500 6.2	X	X
Virtuoso Spectre-RF Simulation Option 6.1	X	X
Virtuoso Spectre-RF Simulation Option 6.2	X	X

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Product Name	Solaris 10 /SPARC	Solaris 10 / x64
Virtuoso UltraSim Full-chip Simulator 6.1	X	X
Virtuoso UltraSim Full-chip Simulator 6.2	X	X
Virtuoso UltraSim Simulator - L 6.2	X	X
Virtuoso Visualization & Analysis XL 6.1	X	X
Virtuoso Waveform Reader Toolbox 6.12	X	X
Virtuoso(R) Layout Suite - GXL 6.12	X	X
VoltageStorm (Gate and Transistor) 06.20.003 06.20.003	X	X
VoltageStorm (transistor) 06.20.003	X	X
VoltageStorm PE 06.20.003	X	X
Waveform 6.1	X	X
Xsim Mixed language software simulator for both Verilog and VHDL 7.1	X	X
Xsim software simulator - Verilog 7.1	X	X
Xsim software simulator upgrade from Verilog to mixed language 7.1	X	X
Xtreme Queing Software option 7.1	X	X
Xtreme Xpert ARM Model with SW debugger option 7.1	X	X
Xtreme/Xcite Library Compiler 7.1	X	X